REMARKS

The Applicant notes that the Office Action indicates at page 2, paragraph 3, that claims 4-6 would be allowable if rewritten in independent form. Accordingly, new independent claim 7 submitted herewith is allowable claim 4 rewritten in independent form. New dependent claims 8 and 9 are allowable claims 5 and 6 rewritten to depend from new allowable claim 7. New claims 7-9 are believed to be allowable.

Claims 1-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chen, *et al.* (U.S. Patent Number 5,432,723). In view of the amendments to the claims and the following remarks, the rejections are respectfully traversed, and reconsideration of the rejections is requested.

In the present invention as claimed in claims 1-6, a digital base booster (DBB) includes an inputting portion and a single data assigner having a single output. The data assigner selects one set of output data from a plurality of sets of output data of the inputting portion and outputs the data to an arithmetic portion.

The claims are amended to clarify certain features of the invention. Specifically, the claims are amended to recite that the single data assigner including the single output selects one set of output data from the plurality of sets of output data of the inputting portion.

Chen, *et al.* discloses X0 REGISTER 81 and X1 REGISTER 82 having bidirectional connections to bus XDB 65 and Y0 REGISTER and Y1 REGISTER having bidirectional connections to bus YDB 64. Each X register has a connection to a first 24-bit input of a multiplier 85, and each Y register has a connection to a second 24-bit input of multiplier 85. Chen, *et al.* is cited in the Office Action as inherently/obviously disclosing multiplexing means for providing corresponding two out of four data (X0, X1, Y0, Y1) from the X and Y registers to the two input of the multiplier 85.

Chen, *et al.* fails to teach or suggest that a single data assigner including a single output selects one set of output data from a plurality of sets of output data of an inputting portion. Instead, in Chen, *et al.*, the multiplier receives two out of four data from the X and Y registers and the input of the multiplier 85, thus Chen, *et al.* cannot include a single data assigner

including a single output.

Chen, *et al.* fails to teach or suggest the elements of the invention set forth in the amended claims. Therefore, it is believed that the amended claims are allowable over the cited reference, and reconsideration of the rejection of claims 1-3 under 35 U.S.C. 103(a) as being unpatentable over Chen, *et al.* is respectfully requested.

In view of the amendments to the claims and the foregoing remarks, it is believed that all claims pending in the application are in condition for allowance, and such allowance is respectfully solicited. If a telephone conference will expedite prosecution of the application, the Examiner is invited to telephone the undersigned.

Respectfully submitted,

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